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DATE: Saturday, August 12, 2006

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<i>DB=PGPB,USPT; PLUR=NO; OP=ADJ</i>			
<input type="checkbox"/>	L69	L68 and (trap\$3 or exception\$1 or interrupt\$3)	6
<input type="checkbox"/>	L68	(6081890 or 5596755 or 6385718 or 6272453 or 6917997 or 5701493 or 5721931).pn.	7
<input type="checkbox"/>	L67	L32 and L2	3
<input type="checkbox"/>	L66	firmware and L64	112
<input type="checkbox"/>	L65	L2 and L64	1
<input type="checkbox"/>	L64	L24 same (L25 or L26)	630
<input type="checkbox"/>	L63	L62 not L58	20
<input type="checkbox"/>	L62	(L55 or L56) and L61	22
<input type="checkbox"/>	L61	(710/260 710/261 710/262 710/263 710/264 710/265 710/266 710/267 710/268 710/269).ccls.	1932
<input type="checkbox"/>	L60	(712/260).ccls.	0
<input type="checkbox"/>	L59	(712/260 712/261 712/262 712/263 712/264 712/265 712/266 712/267 712/268 712/269).ccls.	0
<input type="checkbox"/>	L58	(L55 or L56) and L57	16
<input type="checkbox"/>	L57	(712/244).ccls.	545
<input type="checkbox"/>	L56	(712/229).ccls.	314
<input type="checkbox"/>	L55	(712/43).ccls.	219
<input type="checkbox"/>	L54	6041402.pn.	1
<input type="checkbox"/>	L53	L49 and (interrupt\$1 or exception\$1 or trap\$1)	16
<input type="checkbox"/>	L52	6081890.pn.	1
<input type="checkbox"/>	L51	6272453.pn.	1
<input type="checkbox"/>	L50	6272453.uref.	4
<input type="checkbox"/>	L49	6081890.uref.	26
<input type="checkbox"/>	L48	legacy with L47	8
<input type="checkbox"/>	L47	native with (interrupt\$1 or exception\$1 or trap\$1)	993
<input type="checkbox"/>	L46	multiplexing interrupts	3
<input type="checkbox"/>	L45	native with legacy with (trap\$3 or exception\$1 or interrupt\$3)	8
<input type="checkbox"/>	L44	5608886.pn.	1
<input type="checkbox"/>	L43	dual architecture exception\$1	2
<input type="checkbox"/>	L42	L41 and (interrupt\$3 or exception\$1 or trap\$4)	5

	<i>DB=USPT,PGPB; PLUR=NO; OP=ADJ</i>	
<input type="checkbox"/>	L41 ('5598546' '5638525' '5774686' '5781750' '5930490')![pn]	5
	<i>DB=PGPB,USPT; PLUR=NO; OP=ADJ</i>	
<input type="checkbox"/>	L40 non-native mode	35
<input type="checkbox"/>	L39 (ia-32 with interrupt\$3) and (ia-64 with interrupt\$3)	5
<input type="checkbox"/>	L38 interrupt\$3 and L37	121
<input type="checkbox"/>	L37 ia-32 and ia-64	434
<input type="checkbox"/>	L36 merced and ia-32	13
<input type="checkbox"/>	L35 native same L34	18
<input type="checkbox"/>	L34 x86 with interrupt\$1	227
<input type="checkbox"/>	L33 x86 interrupt\$1	30
<input type="checkbox"/>	L32 (L29 or L30) same L31	147
<input type="checkbox"/>	L31 16 bit\$1 with interrupt\$1	1232
<input type="checkbox"/>	L30 32 bit\$1 with interrupt\$1	751
<input type="checkbox"/>	L29 64 bit\$1 with interrupt\$1	224
<input type="checkbox"/>	L28 interrupt\$1 and L27	28
<input type="checkbox"/>	L27 L24 same L26	89
<input type="checkbox"/>	L26 64 bit\$1 with microprocessor\$1	987
<input type="checkbox"/>	L25 32 bit\$1 with microprocessor\$1	3353
<input type="checkbox"/>	L24 16 bit\$1 with microprocessor\$1	3413
<input type="checkbox"/>	L23 interrupt\$1 and L22	29
<input type="checkbox"/>	L22 L14 and L21	45
<input type="checkbox"/>	L21 32 bit\$1 with processor\$1	7518
<input type="checkbox"/>	L20 L19 and L14	12
<input type="checkbox"/>	L19 64 bit\$1 with processor\$1	2997
<input type="checkbox"/>	L18 L17 not L15	10
<input type="checkbox"/>	L17 L14 same L16	13
<input type="checkbox"/>	L16 32 bit\$1 with processor	6807
<input type="checkbox"/>	L15 L9 and L14	3
<input type="checkbox"/>	L14 16-bit\$1 with coprocessor\$1	119
<input type="checkbox"/>	L13 4349873.pn.	1
<input type="checkbox"/>	L12 interrupt\$1 and L7	12
<input type="checkbox"/>	L11 interrupt\$1 and L6	128
<input type="checkbox"/>	L10 L7 and (L9 or L8)	0
<input type="checkbox"/>	L9 32-bit processor\$1	868
<input type="checkbox"/>	L8 64-bit processor\$1	396
<input type="checkbox"/>	L7 16-bit coprocessor\$1	13
<input type="checkbox"/>	L6 64-bit processor	302

□	L5	32-bit processor	659
□	L4	16-bit coprocessor	12
□	L3	hardware interrupt\$1 same L1	12
□	L2	interrupt\$1 same L1	100
□	L1	real mode\$1 same protected mode\$1	562

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legacy	in Full Text & All Fields	
AND native	in Full Text & All Fields	
AND interrupt	in Full Text & All Fields	



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Relevance scale

1 [Compilation and run-time systems: DELI: a new run-time control point](#)

Giuseppe Desoli, Nikolay Mateev, Evelyn Duesterwald, Paolo Faraboschi, Joseph A. Fisher
November 2002 **Proceedings of the 35th annual ACM/IEEE international symposium on Microarchitecture**

Publisher: IEEE Computer Society Press

Full text available: [pdf\(1.27 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)
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The Dynamic Execution Layer Interface (DELI) offers the following unique capability: it provides fine-grain control over the execution of programs, by allowing its clients to observe and optionally manipulate every single instruction---at run time---just before it runs. DELI accomplishes this by opening up an interface to the layer between the execution of software and hardware. To avoid the slowdown, DELI caches a private copy of the executed code and always runs out of its own private cache. In ...

2 [The Flux OSKit: a substrate for kernel and language research](#)

Bryan Ford, Godmar Back, Greg Benson, Jay Lepreau, Albert Lin, Olin Shivers
October 1997 **ACM SIGOPS Operating Systems Review , Proceedings of the sixteenth ACM symposium on Operating systems principles SOSP '97**, Volume 31 Issue 5

Publisher: ACM Press

Full text available: [pdf\(2.47 MB\)](#) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

3 [Porting RTOS device drivers to embedded Linux](#)

Bill Weinberg
October 2004 **Linux Journal**, Volume 2004 Issue 126

Publisher: Specialized Systems Consultants, Inc.

Full text available: [html\(22.60 KB\)](#) Additional Information: [full citation](#), [abstract](#)

Your old real-time operating system made you do a lot for yourself as a driver author. Take advantage of the facilities Linux offers and clean up some spaghetti code while you're at it.

4 [Dynamic translation: The Transmeta Code Morphing™ Software: using speculation, recovery, and adaptive retranslation to address real-life challenges](#)

James C. Dehnert, Brian K. Grant, John P. Banning, Richard Johnson, Thomas Kistler,